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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 3723
Examiner: Robert A. ROSE

In Re PATENT APPLICATION of:

Applicant(s): Junichi HIKITA et al.

Serial No.: 10/626,524

Filing date: July 25, 2003

For: METHOD OF PRODUCING A
SEMICONDUCTOR DEVICE BY
DIVIDING A SEMICONDUCTOR
WAFER INTO SEPARATE PIECES OF
SEMICONDUCTOR CHIPS

Atty. ref.: AI 290D1 (ROH-0031/DIV)

AMENDMENT

November 22, 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on August 28, 2006, please
amend the above-identified application as follows:

FEE ENCLOSED: \$ 0.00
Please charge any further
fee to our Deposit Account
No. 12-0002